

This listing of claims will replace all prior versions, and listings, of the claims in the application:

Listing of Claims:

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Claim 1 (Original): A signal processing circuit for processing a signal reproduced from a CD, comprising:

a CD-ROM decoder for decoding incoming CD-ROM data by using a memory;
and

an anti-shock controller for causing a predetermined amount of incoming audio data to be stored in said memory, and reading and outputting the audio data from said memory, so that continuous output can be achieved even when the incoming audio data is interrupted; wherein

said CD-ROM decoder and said anti-shock controller access said memory.

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Claim 2 (Original): The circuit according to claim 1, wherein said memory is divided into an area used for decoding the CD-ROM data, and an area where the audio data is stored.

Claim 3 (Currently Amended): The circuit according to claim 1, wherein ~~said memory is entirely used for decoding the CD-ROM data or for storing the audio data~~ for audio data reproduction, the audio data is written into and read from approximately the entire area of said memory, and for CD-ROM data reproduction, the CD-ROM data is written into and read from approximately the entire area of said memory.

Claim 4 (Original): The circuit according to claim 1, wherein
said CD-ROM decoder and said anti-shock controller are connected to said memory through a common interface, and

Appl. No. 09/893,032
Amdt. Dated July 3, 2003
Reply to Office Action of March 5, 2003

Attorney Docket No. 81784.0239

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said interface includes a selection circuit for selecting either a signal from said CD-ROM decoder or a signal from said anti-shock controller.

Claim 5 (Original): The circuit according to claim 1, further comprising an MP3 decoder for decoding data, encoded in MP3 format and output from said CD-ROM decoder, in MP3 format.

Claim 6 (Original): The circuit according to claim 5, further comprising a selection circuit for selecting either an audio signal received from said anti-shock controller or the data encoded in MP3 format received from said MP3 decoder.
